EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	180	703/14.ccls. and @pd>="20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/05/19 10:54
L4	1132	bidirectional and mixed and analog and digital and simulat$5 and circuit	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/05/19 11:46

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… -electrode semiconductor optical amplifiers and analysis of inline photodetection in bidirectional …

A Sharaiha, M Guegan - Journal of Lightwave Technology, 2000 - OSA
… and integration with electrical **circuit** components. … Simultaneous **bidirectional**
photodetection experiments using a three … oper- ation of a **bi-directional** bus made …
Cited by 15 - Related articles - Web Search - BL Direct - All 7 versions

Modeling, simulation, and experimental verification of soft-switched bi-directional DC-DC converters

H Li, FZ Peng, J Lawler - IEEE APEC proceeding, 2001 - ieeexplore.ieee.org
… The proposed technique can be applied to other **bi-directional** DC-DC power … average
model are compared to those of detailed **circuit simulation** and experimental …
Cited by 15 - Related articles - Web Search

[PDF] ▶ A simulation model for floating-gate MOS synapse transistors

K Rahimi, C Diorio, C Hernandez, M Brockhausen - IEEE International Symposium on Circuits and Systems, 2002 - cs.washington.edu
… 7]. To design large-scale synaptic **circuits**, engineers need an accurate SPICE
simulation model for … storage, lo- cally computed **bidirectional** weight updates …
Cited by 26 - Related articles - View as HTML - Web Search - BL Direct - All 7 versions

Comparison of ESD protection capability of lateral BJT, SCR and bidirectional, SCR for hi-voltage …

V Vashchenko, A Concannon, M Ter Beek, P Hopper - Bipolar/BiCMOS Circuits and Technology Meeting, 2002.
…, 2002 - ieeexplore.ieee.org
… using physical process and device **simulation** and pulse … using a cylindrical lateral
SCR and **bi- directional** SCR … for hi-voltage (>24V) BiCMOS **circuits** presents a …
Cited by 10 - Related articles - Web Search - All 3 versions

Scan test data volume reduction in multi-clocked designs with safe capture technique

V Jain, J Waicukauski, S Inc, CA Mountain View - Test Conference, 2002. Proceedings. International, 2002 - ieeexplore.ieee.org
… clock B. Figure 2: An example **circuit** showing clock … of these clocks may lead to
simulation Paper 6.2 … corruption of old data due to **bi-directional** data transfer …
Cited by 16 - Related articles - Web Search - BL Direct - All 4 versions

Teaching nonlinear modeling, simulation, and control of electronic power converters using MATLAB/ …

VF Pires, JFA Silva - Ieee transactions on education, 2002 - ieeexplore.ieee.org
… suited to high power industrial applications and capable of **bidirectional** power
flow … sales/ads/analog/convergence.html [16] DW Hart, "**Circuit simulation** as an …
Cited by 32 - Related articles - Web Search - BL Direct - All 5 versions

Software-based self-testing methodology for processor cores- ▶ upc.es [PDF]

L Chen, S Dey - IEEE Transactions on Computer-Aided Design of Integrated …, 2001 - ieeexplore.ieee.org
… The modifications include: 1) splitting all **bidirectional** pins into … in the normal
operational mode of the **circuit**. … Component-level fault **simulation** is used for …

Cited by 113 - Related articles - Web Search - BL Direct - All 15 versions

A new family of neural network-based bidirectional and dispersive behavioral models for nonlinear RF ...

V Rizzoli, A Neri, D Masotti, A Lipparini - International Journal of RF and Microwave Computer-Aided ..., 2002 - interscience.wiley.com
... In addition, the circuit dynamics is often accounted for by ... In this article, we propose
a bidirectional ANN- based ... DF are computed by the HB simulation [4] and ...
Cited by 9 - Related articles - Web Search - BL Direct - All 2 versions

[PDF] ▶ Toward multiple-valued configurable random molecular logic units

JM Seminario, LE Cordova - Proc. IEEE Nanotechnology Conf, 2001 - che.tamu.edu
... model [14] to include bi-directional operation of an ... diode effects in the bidirectional
model are ... Simulation Program with Integrated Circuit Emphasis) program. ...
Cited by 19 - Related articles - Web Search - All 3 versions

[PDF] ▶ A novel matrix converter topology with simple commutation

L Wei, TA Lipo - CONFERENCE RECORD OF THE IEEE INDUSTRY APPLICATIONS ..., 2001 - ece.wisc.edu
... employed for a circuit level simulation to demonstrate ... previous proposed capacitorless
DC link circuits [3],[4 ... except that the switches are all bidirectional. ...
Cited by 82 - Related articles - View as HTML - Web Search - BL Direct - All 2 versions

Key authors: A Sharaiha - H Li - K Rahimi - V Jain - V Pires

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... -electrode semiconductor optical amplifiers and analysis of inline photodetection in bidirectional ...

A Sharaiha, M Guegan - Journal of Lightwave Technology, 2000 - OSA
... Simultaneous bidirectional photodetection experiments using a ... in presence of
bi-directional transmission," in ... interconnect devices, circuits, and systems ...
Cited by 15 - Related articles - Web Search - BL Direct - All 7 versions

[PDF] ▶ A simulation model for floating-gate MOS synapse transistors

K Rahimi, C Diorio, C Hernandez, M Brockhausen - IEEE International Symposium on Circuits and Systems, 2002 - cs.washington.edu
... 7]. To design large-scale synaptic circuits, engineers need an accurate SPICE
simulation model for ... storage, lo- cally computed bidirectional weight updates ...
Cited by 26 - Related articles - View as HTML - Web Search - BL Direct - All 7 versions

Hierarchical ATPG for analog circuits and systems

M Soma, S Huynh, J Zhang, S Kim, G Devarayanadurg - IEEE Design & Test of Computers, 2001 - ieeexplore.ieee.org
... basis of component/device char- acteristics, simulation, or behavioral ... ds gives rise
to a bidirectional edge ... can be modeled by additional circuit elements whose ...
Cited by 23 - Related articles - Web Search - BL Direct - All 6 versions

Low voltage, low power, high performance current conveyors- ▶ iitd.ac.in [PDF]

SS Rajput, SS Jamuar - Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE ..., 2001 - ieeexplore.ieee.org
... voltage at which the proposed LVCM circuit will operate ... gdl P-Spice simulation of
the LVCM structure ... voltage buffer (LVVB) and a bi-directional LVCM formed ...
Cited by 9 - Related articles - Web Search - BL Direct - All 3 versions

A single-electron-transistor-based analog/digital converter

CH Hu, JF Jiang, QY Cai - Nanotechnology, 2002. IEEE-NANO 2002. Proceedings of the ..., 2002 - ieeexplore.ieee.org
... a modified Tucker's inverter circuit, which has ... 1m-N.rci ?002 Fig. 6. Simulation
results for ... nanowires arid their application in bi-directional electron pumps ...
Cited by 13 - Related articles - Web Search

Global exponential stability in continuous-time and discrete-time delayed bidirectional neural ...

S Mohamad - Physica D: Nonlinear Phenomena, 2001 - Elsevier
... We proceed in this section to consider the dynamical characteristics of a
discrete-time analogue of the continuous-time bidirectional neural network (2.1). ...
Cited by 108 - Related articles - Web Search - All 5 versions

A system-level simulation of complex multi-domain microsystems by using analogue hardware ...

M Jakovljevic, PA Fotiu, Z Mrcarica, H Detter - Sensors & Actuators: A. Physical, 2000 - Elsevier
... in different specific domains, bi-directional coupling is ... applicability of
system-level simulation is improved ... are generalized extensions of circuit simulators ...
Cited by 10 - Related articles - Web Search - All 3 versions

[PDF] ▶ Toward multiple-valued configurable random molecular logic units
JM Seminario, LE Cordova - Proc. IEEE Nanotechnology Conf, 2001 - che.tamu.edu
... model [14] to include **bi-directional** operation of an ... diode effects in the **bidirectional**
model are ... **Simulation** Program with Integrated **Circuit** Emphasis) program. ...
Cited by 19 - Related articles - Web Search - All 3 versions

... Verification for Electromigration Analysis in Arbitrary Shaped Metallization Patterns of
Analog ...- ▶ tu-dresden.de [PDF]
G Jerke, J Lienig - Proceedings of the conference on Design, automation and test ..., 2002 - portal.acm.org
... an increased estimated lifetime for **bi- directional** and pulsed ... simply shaped layout
patterns in CMOS **circuits**. ... only one current- density **simulator** for **analog** ...
Cited by 6 - Related articles - Web Search - All 5 versions

CAD for RF circuits- ▶ psu.edu [PDF]
P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ...,
2001 - portal.acm.org
... other relevant to- pologies, including **bidirectional** or feedback ... numerical techniques
traditionally used in **analog** and RF **circuit simulation**, in particular ...
Cited by 11 - Related articles - Web Search - All 16 versions

Key authors: S Mohamad - D Plant - I Fujimori - M Venditti - E Laprise

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256-channel bidirectional optical interconnect using VCSELs and photodiodes on CMOS

DV Plant, MB Venditti, E Laprise, J Faucher, K ... - Journal of Lightwave Technology, 2001 - OSA
... to our knowledge, to send bidirectional data optically ... Pho- tonics integrated with
VLSI circuits," IEEE J ... characterization of a 2-D bi-directional free-space ...
Cited by 87 - Related articles - Web Search - BL Direct - All 11 versions

A single-electron-transistor-based analog/digital converter

CH Hu, JF Jiang, QY Cai - Nanotechnology, 2002. IEEE-NANO 2002. Proceedings of the ..., 2002 -
ieeexplore.ieee.org
... 1m-N.rci ?002 Fig, 6. Simulation results for ... arid their application in bi-directional
electron pumps ... Single- Electron Majority Logic Circuits", IEICE Trans. ...
Cited by 13 - Related articles - Web Search

Hierarchical ATPG for analog circuits and systems

M Soma, S Huynh, J Zhang, S Kim, G Devarayanadurg - IEEE Design & Test of Computers, 2001 -
ieeexplore.ieee.org
... basis of component/device char- acteristics, simulation, or behavioral ... ds gives rise
to a bidirectional edge ... can be modeled by additional circuit elements whose ...
Cited by 23 - Related articles - Web Search - BL Direct - All 6 versions

A 90-dB SNR 2.5-MHz output-rate ADC using cascaded multibitdelta-sigma modulation at 8×

I Fujimori, L Longo, A Hairapetian, K Seiyama, S ... - IEEE Journal of Solid-State Circuits, 2000 -
ieeexplore.ieee.org
... This is also included in the simulation. ... matching allowed a simple modification
(bi-directional rotation) to ... the switches without any special circuit techniques ...
Cited by 116 - Related articles - Web Search - BL Direct - All 5 versions

CAD for RF circuits- ► psu.edu [PDF]

P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ...,
2001 - portal.acm.org
... other relevant to- pologies, including bidirectional or feedback ... numerical techniques
traditionally used in analog and RF circuit simulation, in particular ...
Cited by 11 - Related articles - Web Search - All 16 versions

A behavioural opto-electro-thermal VCSEL model for simulation of optical links

F Mieyeville, G Jacquemod, F Gaffiot, M Belleville - Sensors & Actuators: A. Physical, 2001 - Elsevier
... a vertical-cavity surface-emitting laser-based bidirectional free-space ... G. Jacquemod,
Behavioral modeling for hierarchical simulation of optronic ... Circuits Syst ...
Cited by 11 - Related articles - Web Search - All 3 versions

[PDF] ► A simulation model for floating-gate MOS synapse transistors

K Rahimi, C Diorio, C Hernandez, M Brockhausen - IEEE International Symposium on Circuits and Systems,
2002 - cs.washington.edu
... 7]. To design large-scale synaptic circuits, engineers need an accurate SPICE
simulation model for ... storage, lo- cally computed bidirectional weight updates ...
Cited by 26 - Related articles - View as HTML - Web Search - BL Direct - All 7 versions

[PDF] ► Toward multiple-valued configurable random molecular logic units
JM Seminario, LE Cordova - Proc. IEEE Nanotechnology Conf, 2001 - che.tamu.edu
... model [14] to include **bi-directional** operation of an ... diode effects in the **bidirectional**
model are ... **Simulation** Program with Integrated **Circuit** Emphasis) program. ...
Cited by 19 - Related articles - Web Search - All 3 versions

A system-level **simulation** of complex multi-domain microsystems by using analogue
hardware ...
M Jakovljevic, PA Fotiu, Z Mrcarica, H Detter - Sensors & Actuators: A. Physical, 2000 - Elsevier
... level modeling consisting of device, behavioral and **circuit** models in ... Heterogeneous
simulation environments. ... in either a unidirectional or a **bi-directional** way. ...
Cited by 10 - Related articles - Web Search - All 3 versions

VLSI implementation of wireless bi-directional communication circuits for micro-stimulator-
► **ncku.edu.tw** [PDF]
SC Li - 2002 - ethesys.lib.ncku.edu.tw
... Figure 2-2. The internal **circuit** blocks of **bi-directional** implantable micro ... and through
the **circuit simulator** to verify all **circuit** functions. If the **simulation** ...
Cited by 4 - Related articles - Web Search - BL Direct - All 2 versions

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Comparison of ESD protection capability of lateral BJT, SCR and **bidirectional** SCR for hi-voltage ...
V Vashchenko, A Concannon, M Ter Beek, P Hopper - Bipolar/BiCMOS Circuits and Technology Meeting, 2002.
..., 2002 - ieeexplore.ieee.org
... of the BiT's and the **bi-directional** SCR's ... BiT (with 1k resistor in the base **circuit**)
(a) and ... obtained by **mixed**-mode thermal coupled transient **simulation**. ...
Cited by 10 - Related articles - Web Search - All 3 versions

Verilog-AMS: **Mixed**-signal **simulation** and cross domain connectmodules- ▶ columbia.edu
[PDF]
P Frey, DO'Riordan, CDS Inc, CA San Jose - ... International Workshop on Behavioral Modeling and **Simulation**
..., 2000 - ieeexplore.ieee.org
Page 1. Verilog- AMS: **Mixed**-Signal **Simulation** and Cross Domain Connect Modules ... extended
Verilog-AMs **mixed**-signal **simulation** capabilities. 1 Introduction ...
Cited by 27 - Related articles - Web Search - All 9 versions

Low voltage, low power, high performance current conveyors- ▶ iitd.ac.in [PDF]
SS Rajput, SS Jamuar - Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE ..., 2001 - ieeexplore.ieee.org
... voltage at which the proposed LVCM **circuit** will operate ... gdi P-Spice **simulation** of
the LVCM structure ... voltage buffer (LVVB) and a **bi-directional** LVCM formed ...
Cited by 9 - Related articles - Web Search - BL Direct - All 3 versions

Time-domain **simulation** of **mixed** nonlinear magnetic and electronicsystems- ▶ soton.ac.uk
[PDF]
AD Brown, JN Ross, KG Nichols - IEEE Transactions on Magnetics, 2001 - ieeexplore.ieee.org
... electromagnetic inter- actions are **bidirectional**) and analyzed ... extended to include
magnetic **circuit** variables, allowing ... et al.: TIME-DOMAIN **SIMULATION** OF **MIXED** ...
Cited by 5 - Related articles - Web Search - BL Direct - All 9 versions

A behavioural opto-electro-thermal VCSEL model for **simulation** of optical links
F Mieyeville, G Jacquemod, F Gaffiot, M Belleville - Sensors & Actuators: A. Physical, 2001 - Elsevier
... a vertical-cavity surface-emitting laser-based **bidirectional** free-space ... G. Jacquemod,
Behavioral modeling for hierarchical **simulation** of optronic ... **Circuits** Syst ...
Cited by 11 - Related articles - Web Search - All 3 versions

CAD for RF circuits- ▶ psu.edu [PDF]
P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ...,
2001 - portal.acm.org
... the **simulation** of a complex **mixed**-signal sys ... other relevant to- pologies, including
bidirectional or feedback ... commonly used in RF **circuit simulation** are the ...
Cited by 11 - Related articles - Web Search - All 16 versions

Electrical and electrothermal 2D simulations of a 4H-SiC highvoltage current limiting device for
...
F Nallet, A Senes, D Planson, ML Locatelli, JP ... - Power Electronics and Motion Control Conference, 2000. ...,
2000 - ieeexplore.ieee.org
... study the ability of a **bi-directional** current limiting ... The current conduction is

bidirectional but the current ... The **circuit** used for the **mixed** mode **simulation** ...
Cited by 5 - Related articles - Web Search - All 3 versions

Bidirectional buffer with active pull-up/latch **circuit for mixed**-voltage applications
HC Chow, CH Chien - US Patent 6,060,906, 2000 - Google Patents
... 11 illustrates voltage over time **simulation** results for the **circuit** of FIG. ... 5 shows
a **bidirectional** multi-voltage buffer **circuit** 300 according to a first ...
Cited by 3 - Related articles - Web Search - All 2 versions

A 1.1-V 270-/spl mu/A **mixed**-signal hearing aid chip
DG Gata, W Sjursen, JR Hochschild, JW Fattaruso, L ... - IEEE Journal of Solid-State Circuits, 2002 -
ieeexplore.ieee.org
... The **bidirectional** shift registers in Fig ... GATA et al.: 1.1-V 270- A **MIXED**-SIGNAL HEARING
AID ... include analog and RF **circuit** design, **circuit simulation** and optimiza ...
Cited by 11 - Related articles - Web Search - BL Direct - All 3 versions

A switch level fault **simulation** environment- ▶ **uci.edu** [PDF]
V Krishnaswamy, J Casas, T Tetzlaff - Design Automation Conference, 2000. Proceedings 2000. 37th, 2000 -
ieeexplore.ieee.org
... The fault **simulation** algorithm places no restrictions on the **circuit** styles used
to implement designs. **Mixed** level simula- tion issues are discussed. ...
Cited by 9 - Related articles - Web Search - BL Direct - All 15 versions

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Sc

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Verilog-AMS: **Mixed**-signal **simulation** and cross domain connectmodules- ► columbia.edu
[PDF]

P Frey, DO'Riordan, CDS Inc, CA San Jose - ... International Workshop on Behavioral Modeling and **Simulation**
..., 2000 - ieeexplore.ieee.org
Verilog- AMS: **Mixed**-Signal **Simulation** and Cross Domain Connect Modules Peter Frey
and Donald O'Riordan **Cadence** Design Systems, Inc.; 555 River Oaks Parkway ...
Cited by 27 - Related articles - Web Search - All 9 versions

Multithreaded, **mixed** hardware description languages logic **simulation** on engineering workstations
T Chan - US Patent 6,466,898, 2002 - Google Patents
... X Yes | No Apply stimulus to **circuit** signals Evaluate ... multi-phase clock # define
these for **mixed** event-driven and cycle-based **simulation** # Cycle =(CLK ...
Cited by 16 - Related articles - Web Search - All 2 versions

Extending Spice-like analog simulator with a time-domain full-wavefield solver
T Li, W Sui, CDS Inc, N Providence - 2001 IEEE MTT-S International Microwave Symposium Digest, 2001 -
ieeexplore.ieee.org
... dimensions, traditional analog or **mixed**-signal simulators ... and field (distributive)
simulation through a simple and stable **bi-directional circuit**-field model. ...
Cited by 1 - Related articles - Web Search - BL Direct - All 2 versions

CAD for RF circuits- ► psu.edu [PDF]
P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ...,
2001 - portal.acm.org
... the **simulation** of a complex **mixed**-signal sys ... other relevant to- pologies, including
bidirectional or feedback ... commonly used in RF **circuit simulation** are the ...
Cited by 11 - Related articles - Web Search - All 16 versions

Bi-directional **mixed** signal connection modules for automatic insertion
O Zinke, CD Syst, CA San Diego - Behavioral Modeling and **Simulation**, 2002. BMAS 2002. ..., 2002 -
ieeexplore.ieee.org
BI-DIRECTIONAL MIXED SIGNAL CONNECTION MODULES FOR AUTOMATIC ... Abstract Efficient
simulation of **mixed** signal ... describes how true **bidirectional** connect modules can ...
Cited by 1 - Related articles - Web Search

VLSI implementation of wireless bi-directional communication circuits for micro-stimulator- ► ncku.edu.tw [PDF]
SC Li - 2002 - ethesys.lib.ncku.edu.tw
... Figure 2-2. The internal **circuit** blocks of **bi-directional** implantable micro ... and through
the **circuit simulator** to verify all **circuit** functions. If the **simulation** ...
Cited by 4 - Related articles - Web Search - BL Direct - All 2 versions

[BOOK] Model engineering in **mixed**-signal **circuit** design: a guide to generating accurate behavioral models ...
SA Huss - 2001 - books.google.com
... 99 5.8 Generic VHDL-AMS model code for a 1 bit D/A converter 100 5.9 **Simulation**

of **mixed**-signal **circuits** 100 5.10 Step response of a dynamic system 103 ...
Cited by 2 - Related articles - Web Search - Library Search - All 2 versions

Teaching nonlinear modeling, **simulation**, and control of electronic power converters using MATLAB/ ...

VF Pires, JFA Silva - Ieee transactions on education, 2002 - ieeexplore.ieee.org
... power industrial applications and capable of **bidirectional** power flow ... (2001, September)
Simplorer **simulation** system ... 2001, September 6) SPECTRE **circuit simulator**. ...
Cited by 32 - Related articles - Web Search - BL Direct - All 5 versions

Hierarchical ATPG for analog circuits and systems

M Soma, S Huynh, J Zhang, S Kim, G Devarayanadurg - IEEE Design & Test of Computers, 2001 -
ieeexplore.ieee.org
... basis of component/device char- acteristics, **simulation**, or behavioral ... ds gives rise
to a **bidirectional** edge ... can be modeled by additional **circuit** elements whose ...
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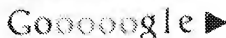
J Roychowdhury - Low-Power Design Techniques and CAD Tools for Analog and RF ..., 2001 -
books.google.com
... the t]-t2 plane, or using a **mixed** time-frequency ... 2fO Figure 2.16 HPSDs of
mixer-filter-mixer **circuit**. ... ANALYSIS AND **SIMULATION** OF RF SUBSYSTEMS 31 This transfer ...
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An RTL abstraction technique for processor microarchitecture validation and test generation
J Shen, JA Abraham - Journal of Electronic Testing, 2000 - Springer
... Current address: Jian Shen is now with **Cadence** Design Systems ... The AVPGEN test generation
system from **Chandra** [13] uses constraint solving to choose initial data ...
Cited by 37 - Related articles - Web Search - BL Direct - All 4 versions

On concurrent test of core-based SOC design
Y Huang, WT Cheng, CC Tsai, N Mukherjee, O Samman, ... - Journal of Electronic Testing, 2002 - Springer
... cores, therefore, it can be tested concurrently with ei- ther Core 1 , Core 2 ,
or Core 3 . In order to reduce the required number of **bi-directional** SOC pins ...
Cited by 22 - Related articles - Web Search - BL Direct - All 3 versions

Astrophysics in 1999
V Trimble, MJ Aschwanden - Publications of the Astronomical Society of the Pacific, 2000 - UChicago Press
... 1999), and the X-ray satellite **Chandra** (formerly AXAF ... Theoretical models of
bi-directional EUV jets ejected from ... to detect (because a high **cadence** and contrast ...
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Key authors: **J Shen** - **J Abraham** - **Y Huang** - **W Cheng** - **C Tsai**

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Electron densities in solar flare loops, chromospheric evaporation upflows, and acceleration sites- ▶ psu.edu [PDF]
MJ Aschwanden, AO Benz - The Astrophysical Journal, 1997 - UChicago Press
... site can be measured from broadband radio spectra that show **bidirectional** electron
beams ... in a limited field of view with 64 × 64 pixels, with a **cadence** of a ...
Cited by 58 - Related articles - Web Search - Library Search - BL Direct - All 11 versions

An RTL abstraction technique for processor microarchitecture validation and test generation
J Shen, JA Abraham - Journal of Electronic Testing, 2000 - Springer
... Current address: Jian Shen is now with **Cadence** Design Systems ... The AVPGEN test generation
system from **Chandra** [13] uses constraint solving to choose initial data ...
Cited by 37 - Related articles - Web Search - BL Direct - All 4 versions

On concurrent test of core-based SOC design
Y Huang, WT Cheng, CC Tsai, N Mukherjee, O Samman, ... - Journal of Electronic Testing, 2002 - Springer
... cores, therefore, it can be tested concurrently with ei- ther Core 1 , Core 2 ,
or Core 3 . In order to reduce the required number of **bi- directional** SOC pins ...
Cited by 22 - Related articles - Web Search - BL Direct - All 3 versions

Astrophysics in 1999
V Trimble, MJ Aschwanden - Publications of the Astronomical Society of the Pacific, 2000 - UChicago Press
... 1999), and the X-ray satellite **Chandra** (formerly AXAF ... Theoretical models of
bi-directional EUV jets ejected from ... to detect (because a high **cadence** and contrast ...
Cited by 1 - Related articles - Web Search - BL Direct - All 3 versions

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Digital Object Identifier 10.1109/TAES.2000.869528
AbstractPlus | Full Text: PDF(23324 KB) IEEE JNL
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☐ 3. **Neuromorphic CMOS circuitry for active bidirectional delay lines**
Yang, W.;
Circuits and Systems, 1996. ISCAS '96., 'Connecting the World'., 1996 IEEE
Volume 3, 12-15 May 1996 Page(s):473 - 477 vol.3
Digital Object Identifier 10.1109/ISCAS.1996.541636
AbstractPlus | Full Text: PDF(404 KB) IEEE CNF
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☐ 4. **A new bidirectional associative memory based on distributed represents**
Jiongtao Huang; Hagiwara, M.;
Neural Networks, 1999. IJCNN '99. International Joint Conference on
Volume 2, 10-16 July 1999 Page(s):758 - 763 vol.2
Digital Object Identifier 10.1109/IJCNN.1999.831044
AbstractPlus | Full Text: PDF(424 KB) IEEE CNF
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☐ 5. **Multi-winners self-organizing bidirectional associative memory**
Jiongtao Huang; Hagiwara, M.;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference c
Volume 4, 11-14 Oct. 1998 Page(s):3641 - 3646 vol.4

Digital Object Identifier 10.1109/ICSMC.1998.726631

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   Volume 36, Issue 25, Part Supplement, December 2000 Page(s):2127 - 21£

   AbstractPlus | Full Text: PDF(5180 KB)   IET JNL

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   Ohtani, M.; Asai, T.; Yonezu, H.; Ohshima, N.;
   Microelectronics for Neural, Fuzzy and Bio-Inspired Systems, 1999. MicroNeu
   Seventh International Conference on
   7-9 April 1999 Page(s):366 - 373
   Digital Object Identifier 10.1109/MN.1999.758888

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8. **Evaluating mixed-signal simulators**
   Overhauser, D.; Saleh, R.;
   Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995
   1-4 May 1995 Page(s):113 - 120
   Digital Object Identifier 10.1109/CICC.1995.518148

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   Linares-Barranco, B.; Sanchez-Sinencio, E.; Rodriguez-Vazquez, A.; Huertas
   Neural Networks, IEEE Transactions on
   Volume 4, Issue 3, May 1993 Page(s):445 - 455
   Digital Object Identifier 10.1109/72.217187

   AbstractPlus | Full Text: PDF(852 KB)   IEEE JNL
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10. **VLSI implementation of a transconductance mode continuous BAM with**
    dynamic analog memory
    Linares-Barranco, B.; Sanchez-Sinencio, E.; Rodriguez-Vazquez, A.; Huertas
    Circuits and Systems, 1991., IEEE International Sympoisum on
    11-14 June 1991 Page(s):1283 - 1286 vol.2
    Digital Object Identifier 10.1109/ISCAS.1991.176604

    AbstractPlus | Full Text: PDF(292 KB)   IEEE CNF
    Rights and Permissions

11. **A CDMA based bidirectional communication system for hybrid fiber-coa**
    De Jong, Y.L.C.; Wolters, R.P.C.; Van den Boom, H.P.A.;
    Broadcasting, IEEE Transactions on
    Volume 43, Issue 2, June 1997 Page(s):127 - 135
    Digital Object Identifier 10.1109/11.598361

    AbstractPlus | Full Text: PDF(804 KB)   IEEE JNL
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12. **1997 Combined Index**
    Microwave Theory and Techniques, IEEE Transactions on
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2000
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☐ **15. 1995 Index IEEE Transactions on Circuits and Systems I: Fundamental 1**
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Circuits and Systems I: Fundamental Theory and Applications, IEEE Transac
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Digital Object Identifier 10.1109/TCSI.1995.481202
AbstractPlus | Full Text: PDF(2168 KB)   IEEE JNL
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☐ **16. The Grand Alliance system for US HDTV**
Challapali, K.; Lebegue, X.; Lim, J.S.; Paik, W.H.; Snopko, P.A.;
Proceedings of the IEEE
Volume 83, Issue 2, Feb. 1995 Page(s):158 - 174
Digital Object Identifier 10.1109/5.364468
AbstractPlus | Full Text: PDF(1508 KB)   IEEE JNL
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☐ **17. 1996 Combined Index IEEE Transactions on Microwave Theory and Tecl**
MTT-S Sponsored Conferences
Microwave Theory and Techniques, IEEE Transactions on
Volume 44, Issue 12, Part 2, Dec. 1996 Page(s):2763 - 2873
Digital Object Identifier 10.1109/TMTT.1996.554663
AbstractPlus | Full Text: PDF(18212 KB)   IEEE JNL
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☐ **18. Subject Index**
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactio
Volume 18, Issue 12, Dec. 1999 Page(s):6 - 20
Digital Object Identifier 10.1109/TCAD.1999.811332
AbstractPlus | Full Text: PDF(244 KB)   IEEE JNL
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☐ **19. Multi-winners self-organizing episodic associative memory**
Jiongtao Huang; Hagiwara, M.;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference c
Volume 4, 11-14 Oct. 1998 Page(s):3635 - 3640 vol.4
Digital Object Identifier 10.1109/ICSMC.1998.726630
AbstractPlus | Full Text: PDF(444 KB)   IEEE CNF
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☐ **20.** B
2000
Digital Object Identifier 10.1109/IEEESTD.2000.322233
AbstractPlus | Full Text: PDF(519 KB)   IEEE STD

☐ **21. Low control voltage programming of floating gate MOSFETs and applic:**
Thomsen, A.; Brooke, M.A.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transac
Volume 41, Issue 6, June 1994 Page(s):443 - 452
Digital Object Identifier 10.1109/81.295240

AbstractPlus | Full Text: PDF(536 KB)   IEEE JNL
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22. **Author Index**
Lightwave Technology, Journal of
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Digital Object Identifier 10.1109/JLT.1998.736641

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23. **A PC-based simulation and DSP-based control for an improved high fre**
inverter induction motor drive
Lai, J.-S.; Bose, B.K.;
Industrial Electronics Society, 1990. IECON '90., 16th Annual Conference of I
27-30 Nov. 1990 Page(s):882 - 888 vol.2
Digital Object Identifier 10.1109/IECON.1990.149256

AbstractPlus | Full Text: PDF(544 KB)   IEEE CNF
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24. **1997 Index**
Lightwave Technology, Journal of
Volume 15, Issue 12, Dec. 1997 Page(s):0_5 - 0_45
Digital Object Identifier 10.1109/JLT.1997.643614

AbstractPlus | Full Text: PDF(1760 KB)   IEEE JNL
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25. **VLSI architecture for analog bidirectional pulse-coupled neural networks**
Ota, Y.; Wilamowski, B.M.;
Neural Networks,1997., International Conference on
Volume 2, 9-12 June 1997 Page(s):964 - 968 vol.2
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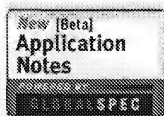
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☐ 2. **Bidirectional four-wave mixing in semiconductor optical amplifiers: theo**
Bischoff, S.; Buxens, A.; Poulsen, H.N.; Clausen, A.T.; Mork, J.;
Lightwave Technology, Journal of
Volume 17, Issue 9, Sept. 1999 Page(s):1617 - 1625
Digital Object Identifier 10.1109/50.788567
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[Subject Index]
Aerospace and Electronic Systems, IEEE Transactions on
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AbstractPlus | Full Text: PDF(23324 KB)   IEEE JNL
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☐ 4. **Unmixing the directional reflectances of AVHRR sub-pixel landcovers**
Asner, G.P.; Wessman, C.A.; Privette, J.L.;
Geoscience and Remote Sensing, IEEE Transactions on
Volume 35, Issue 4, July 1997 Page(s):868 - 878
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AbstractPlus | Full Text: PDF(5180 KB)   IET JNL

6. **Partitioning and analysis of static digital CMOS circuits**
Hubner, U.; Vierhaus, H.T.; Camposano, R.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 16, Issue 11, Nov. 1997 Page(s):1292 - 1310
Digital Object Identifier 10.1109/43.663819

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7. **IEEE standard hardware description language based on the Verilog(R) h**
language
14 Oct. 1996

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8. **1997 Combined Index**
Microwave Theory and Techniques, IEEE Transactions on
Volume 45, Issue 12, Part 2, Dec. 1997 Page(s):2555 - 2664
Digital Object Identifier 10.1109/TMTT.1997.643873

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Hwang-Cherng Chow;
Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE Intern
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Volume 46, Issue 6, Dec. 1999 Page(s):5 - 18
Digital Object Identifier 10.1109/TIE.1999.808022

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11. **Exploiting symmetry when verifying transistor-level circuits by symboli**
Pandey, M.; Bryant, R.E.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactio
Volume 18, Issue 7, July 1999 Page(s):918 - 935
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13. **1997 Index**
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Volume 9, Issue 12, Dec. 1997 Page(s):0_5 - 0_92
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Digital Object Identifier 10.1109/TIA.1998.739033

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Cohen, M.H.; Andreou, A.G.;
Solid-State Circuits, IEEE Journal of
Volume 27, Issue 5, May 1992 Page(s):714 - 727
Digital Object Identifier 10.1109/4.133158

AbstractPlus | Full Text: PDF(1072 KB)   IEEE JNL
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Sun, G.; Ranson, K.J.;
Geoscience and Remote Sensing, IEEE Transactions on
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Kani, J.; Jinno, M.; Sakamoto, T.; Aisawa, S.; Fukui, M.; Hattori, K.; Oguchi, K
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Digital Object Identifier 10.1109/50.803017

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20. **A CMOS mixed signal simultaneous bidirectional signaling I/O**
Jackson, S.A.; Blalock, B.J.;
Circuits and Systems, 1998. Proceedings. 1998 Midwest Symposium on
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Digital Object Identifier 10.1109/MWSCAS.1998.759430

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    Optics and Photonics Vol.37 (IEEE Cat. No. 00CH37079)
    Optical Fiber Communication Conference, 2000
    Volume 2, 7-10 March 2000
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